



IFW

PATENT
790001-2035

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s) : Masaru Kito et al.
U.S. Serial No. : 10/644,415
Filing Date : August 20, 2003
For : ***SEMICONDUCTOR MEMORY DEVICE WITH SURFACE
STRAP AND METHOD OF FABRICATING THE SAME***
Examiner : Tu Tu V. Ho
Group Art Unit : 2818

745 Fifth Avenue
New York, NY 10151

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on August 23, 2004.

Bruno Polito, Reg. No. 38,580

(Name of Applicant, Assignee or Registered Representative)

Signature

August 23, 2004

Date of Signature

RESPONSE TO RESTRICTION REQUIREMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Dear Sir:

In response to the Office Action mailed July 23, 2004, setting a one-month period for reply and asserting a restriction requirement, Applicants elect Group I (claims 1-6) without traverse, for prosecution in this application.

Applicants reserve the right to file divisional applications to non-elected subject matter.

If any issues remain, or if the Examiner has any further suggestions, he/she is invited to call the undersigned at the telephone number provided below.

The Examiner is hereby authorized to charge any insufficient fees or credit any overpayment associated with the above-identified application to Deposit Account No. 50-0320.

The Examiner's consideration of this matter is gratefully acknowledged.

Respectfully submitted,

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By: 

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